

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT				Atty. Docket No. XA-10260		Appln. No. 10/521,552	
				Applicant Tetsuya KAMEI et al.			
				Filing Date March 20, 2005		Group 2115	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
TN	AA	6,389,527	05/14/02	Raam et al.	712	38	
	AB						
	AC						
	AD						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
TN	AE	2000-231550	08/22/00	Japan			No
	AF	10-63502	03/06/98	Japan			Abstract
	AG	4-195448	07/15/92	Japan			Abstract
	AH	0 952 428	10/2799	Europe			
TN	AI	2000-231549	08/22/00	Japan			Abstract
	AJ						
	AK						
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER (including author, title, date, pertinent pages, etc.)							
TN	AQ	M. ISHIKAWA et al., "A Methodology of Low Power On-Chip Memory Access in Embedded Microprocessor for Mobile Applications," The Institute of Electronics, Information and Communication Engineers, Vol. 102, No. 234 (ICD2002 35-45), July 18, 2002, pp. 1-6.					
TN	AR	SH7750 Programming Manual, Hitachi, Ltd., published April 1998, pages 4-9, 10-128, and 10-130.					
Examiner /Than Nguyen/				Date Considered 01/04/2007			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							